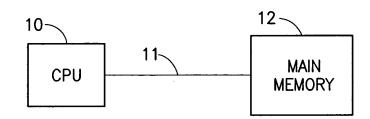


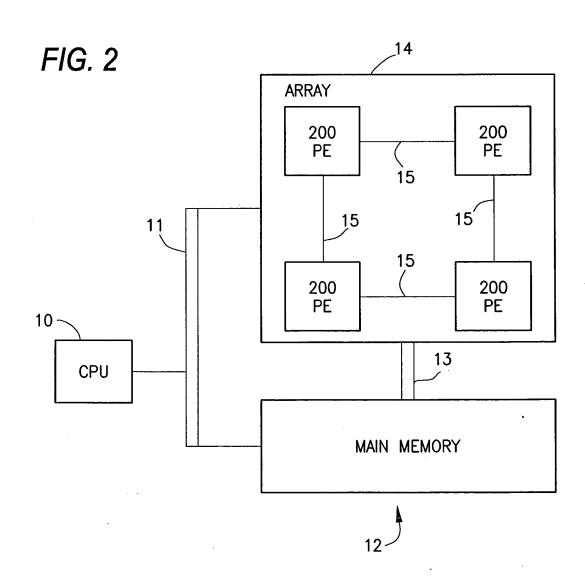
Docket No.: M4065.0435/P435

App No.: 09/874,307 Docket No.: M4065.
Inventor: Graham Kirsch
Title: METHOD AND CIRCUIT FOR ALIGNMENT OF

FLOATING POINT SIGNIFICANTS IN A SIMD ARRAY MPP

FIG. 1 (PRIOR ART)





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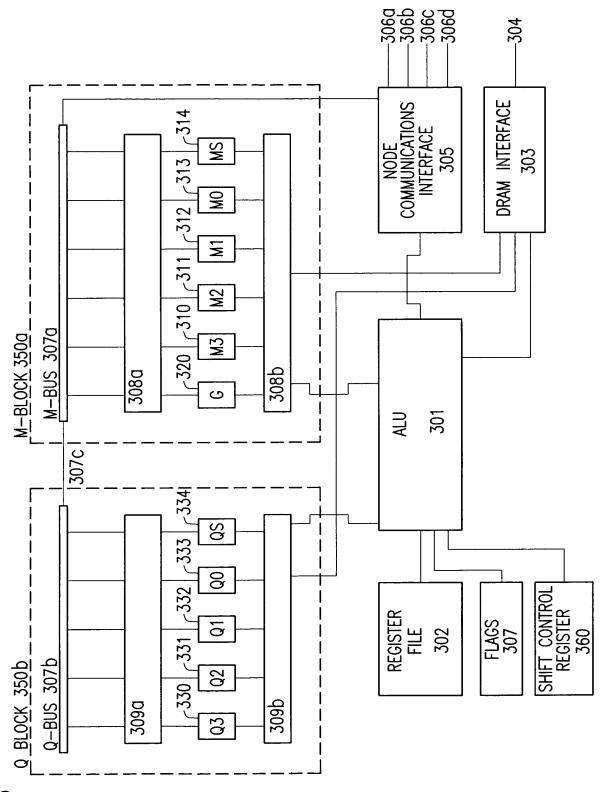
App No.: 09/874,307

Docket No.: M4065.0435/P435

Inventor: Graham Kirsch

Title: METHOD AND CIRCUIT FOR ALIGNMENT OF

FLOATING POINT SIGNIFICANTS IN A SIMD ARRAY MPP



F/G. 3

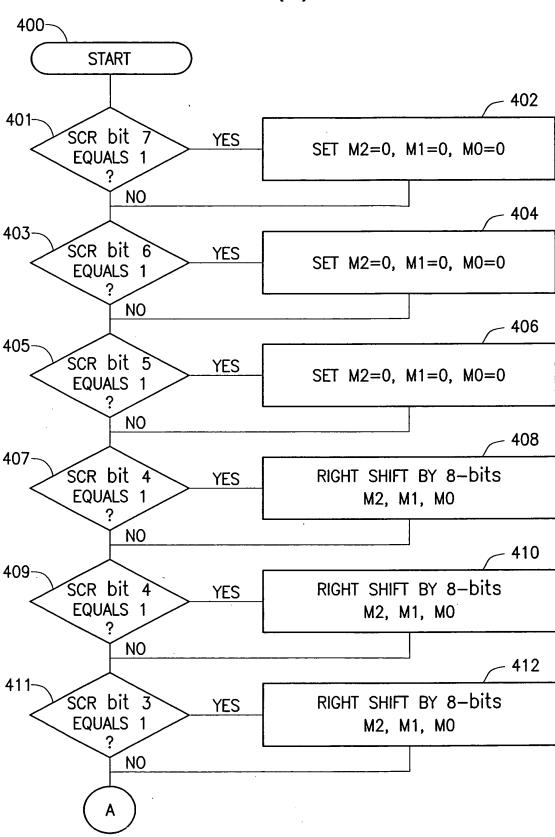


App No.: 09/874,307 Inventor: Graham Kirsch Docket No.: M4065.0435/P435

Title: METHOD AND CIRCUIT FOR ALIGNMENT OF

FLOATING POINT SIGNIFICANTS IN A SIMD ARRAY MPP

FIG. 4(A)



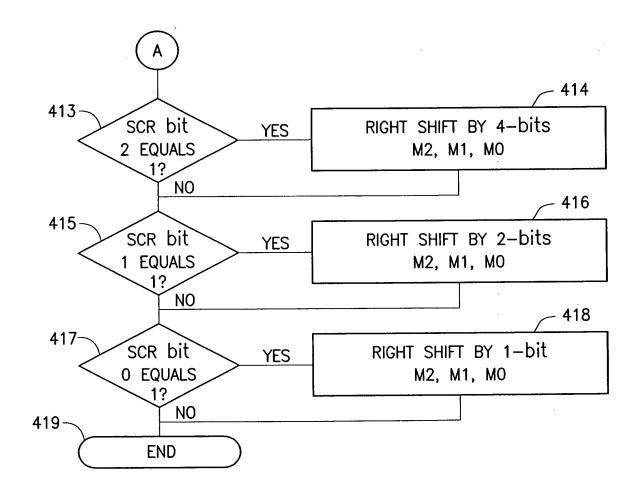
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App No.: 09/874,307 Inventor: Graham Kirsch Docket No.: M4065.0435/P435

Title: METHOD AND CIRCUIT FOR ALIGNMENT OF

FLOATING POINT SIGNIFICANTS IN A SIMD ARRAY MPP

FIG. 4(B)



App No.: 09/874,307 Docket No.: M4065.0435/P435 Inventor: Graham Kirsch Title: METHOD AND CIRCUIT FOR ALIGNMENT OF FLOATING POINT SIGNIFICANTS IN A SIMD ARRAY MPP (AS AMENDED) 500 FIG. 5 **START** 501 -SET TEMP=0 503 502-LEFT SHIFT RESULT BY 8-bits YES Q2Z8=1? TEMP=TEMP+8 NO 505 504 LEFT SHIFT RESULT BY 8-bits YES Q2Z8=1? TEMP=TEMP+8 NO 507 506-LEFT SHIFT RESULT BY 8-bits YES Q2Z8=1? TEMP=TEMP+8 NO 509 508-LEFT SHIFT RESULT BY 4-bits YES Q2Z4=1? TEMP=TEMP+4 NO 511 510-YES LEFT SHIFT RESULT BY 2-bits Q2Z2=1?TEMP=TEMP+2 NO 513 512-LEFT SHIFT RESULT BY 1-bit YES Q2Z1=1?TEMP=TEMP+1 NO 514-Q3=Q3-TEMP515 -**END**